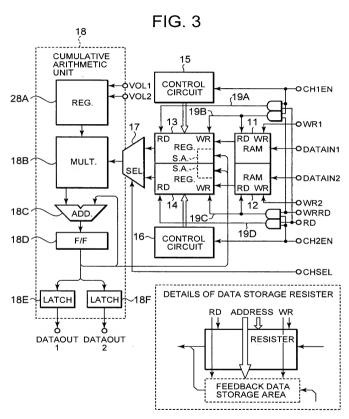
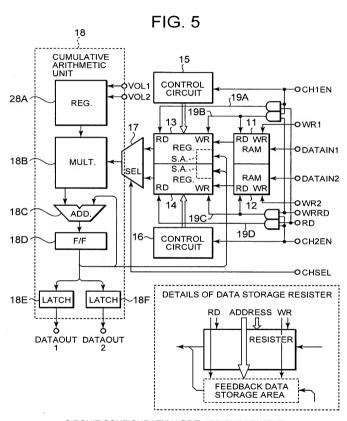


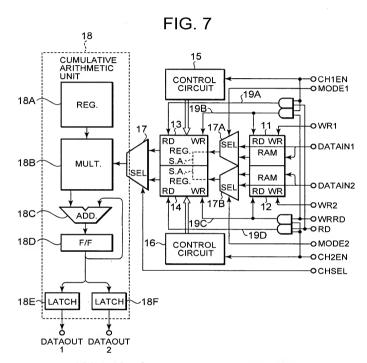
CIRCUIT CONFIGURATION OF FIRST EMBODIMENT



CIRCUIT CONFIGURATION OF SECOND EMBODIMENT



CIRCUIT CONFIGURATION OF THIRD EMBODIMENT



CIRCUIT CONFIGURATION OF FOURTH EMBODIMENT